

# 如何撰写高质量的科技论文

## 阮新波

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二 写科技论文的意义



**科技论文的基本结构** 



**国际期刊评审论文的标准** 



**型提高论文质量的方法** 



二 结语



## 二 写科技论文的意义

**2** 科技论文的基本结构

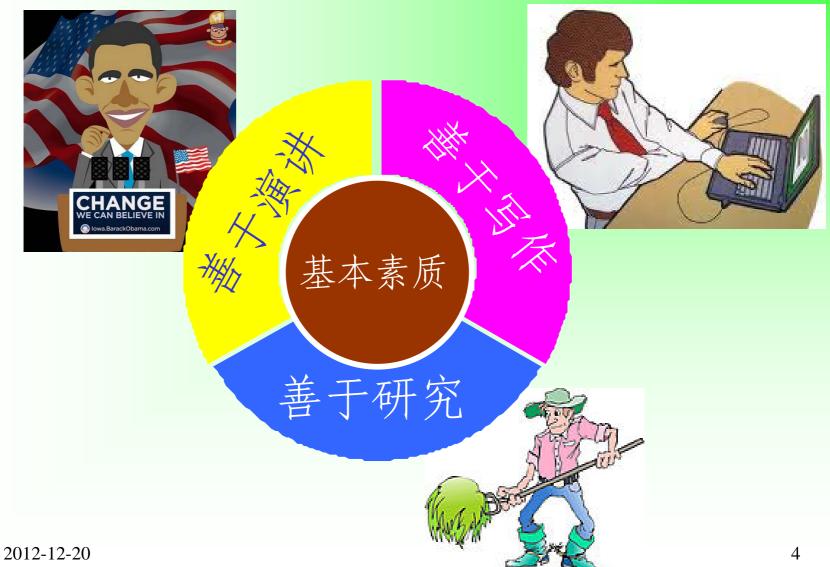
**鱼际期刊评审论文的标准** 

型提高论文质量的方法

乙结语



## 优秀学生应具备的基本素质







- 科技论文是对科学研究中提出的新思想、新方法、新概念等的总结和阐述。
- 发表论文是为了与同行分享所取得的科研成果,是对本领域的贡献。
- 发表论文是获得同行认可、建立学术地位的一种途径。

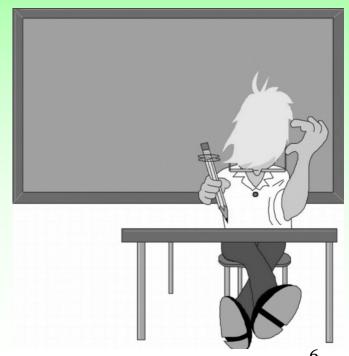
是职称晋升、学生毕业的一个重要指标。







- 撰写科技论文是对研究工作的整理、再认识, 可以提高自己 的总结能力;
- 有利于弄清某些在研究中似是而非、没有完全明白的问题, 是研究过程的重要组成部分;
- 撰写论文的过程中, 甚至可以产 生新的灵感。
- 有利于形成良好的思维方式。







**当**写科技论文的意义



**科技论文的基本结构** 



**四**国际期刊评审论文的标准



立提高论文质量的方法



乙结语



## 论文题目(Paper Title)

作者(Authors)

- 摘要(Abstract)
- 关键词(Index Terms)
- 正文(Main Body)
- 结论(Conclusion)

- 致谢(Acknowledgment)
- 参考文献(References)
- 作者简介(Biography of Authors)



作者

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参考文献

作者简介

论文题目要充分、合适、具体地反映论文的主要内容、 主要贡献,切忌太长、太大、太空,慎用"新颖的"等词 汇,最好不用缩写。

- Means of Eliminating Electrolytic Capacitor in AC/DC Power
  Supplies for LED Lightings
  - Variable-Duty-Cycle Control to Achieve High Input Power Factor for DCM Boost PFC Converter
  - □ 直流变换器的研究
  - □ 一种新颖的感应电机控制方法
  - An DEM for Buck-Boost Converter (DEM: Dual Edge Modulation)

 $\odot$ 



## 论文题目 作者

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- 只有对本文作出贡献的作者才能署名;
- 署名顺序按贡献大小排列,第一作者应为本文的主要贡献者;
- 本研究工作的经费资助者如果对本文没有贡献,不可以作为合作作者署名。但须在致谢中表达谢意。



# 论文题目 作者 擴要

关键词 正文 结论

参考文献

致谢

作者简介

- 摘要主要说明本文的主题、提出的方法和得出的结论。
- 摘要自身应成为一个有价值的文献,特别要包括本文的关键词,让读者在阅读本文前就大致知道本文的主要内容和贡献,而且容易被搜索引擎搜索到。
- 摘要中不能包括公式、参考文献。
- 摘要一般不超过200个单词。



作者

摘要

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作者简介

This paper proposes two methods of reducing the storage capacitance in the ac/dc power supplies for light emitting diode (LED) lighting. In doing so, film capacitors can be adopted instead of electrolytic capacitors to achieve a long power supplies' lifetime. The voltage ripple of the storage capacitor is intentionally increased to reduce the storage capacitance. The method of determining the storage capacitance for ensuring that the boost power factor correction converter operates normally in the whole input voltage range is also discussed. For the purpose of further reducing the storage capacitance, a method of injecting the third harmonic current into the input current flow is proposed. While ensuring that the input power factor is always higher than 0.9 to comply with regulation standards such as ENERGY STAR, the storage capacitance can be reduced to 65.6% of that with an input power factor of 1. A 60-W experimental prototype is built to verify the proposed methods.



## 

## 关键词

正文

结论

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参考文献

作者简介

- 关键词必须表明本文的相关领域、主题。
- 关键词一般为专业词汇和通用的词汇,以便于搜索 引擎搜索到。
- 关键词一般为3~5个。

Harmonic current injection, Light-Emitted Diode (LED), power factor correction (PFC), power supply, voltage ripple.



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作者简介

- 明确本文的**研究对象**及其**研究意义**(重要性、应用前景)。
- 对相关研究工作进行恰当评述(国内外现状),并指出 存在的问题和不足。
- 本文的主要贡献(针对什么问题提出什么方法,结果如何)。
- 简要说明本文的架构。
- ★ 引言是论文最难写的部分。
- ★ 需要有较大的阅读量,尤其是阅读相关领域重要期刊和会议论文。



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作者简介

The rapid developm bened up new general illum **LED**的优点t, thanks to opportunities in the distinct advantages such as high efficacy, long meume, environmental menantiess, and small size over incandescent and fluorescent lamps [1]–[4]. The power supply for LED lighting is an ac/dc converter, which convertes a regular ac voltage to a low dc voltage for an LED le LED 电源需要 Is an impo rtant requirement of [5]. Thus, the ac/dc converter must typically have the function of power factor correction (PFC). In a PFC converter, the input current is forced to be in phase with the input voltage, leading to a pulsating input power, while the output power is constant. To achie PFC变换器需要很大的储能电容 pacitance is required for balancing the ins high capacity required for capacitance, an electrolytic apacitor is often used as the storage capacitor. However, it is well known that because of its liquid electrolyte, the lifetime of an electrolytic capacitor is quite limited with only several thousand hours under rated operating 储能电容寿命较短, conserve tive design, the theoretical lifetime (与LED的长寿命不相匹配 1000 h (at a high operating LED's temperature) [6]. This is much shorter than the potential lifetime of LEDs (50 000 h). Thus, the electrolytic capacitor is an obstacle to the overall long-term reliability of the LED and its power supply.



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作者简介

A review of literature shows that a variety of LED power supplies and driver solutions, which can accurately control the current of the LED while achieving a near-unity input power factor, have been proposed [7]—[10]. However, an electrolytic capacitation of the LED while achieving a proposed [7]—[10]. However, an electrolytic capacitation of the LED while achieving a proposed [7]—[10].

PFC converters 指出这些方案都不能减小储能电容 ind single-stage.

Two-stage PFC converters consist of a PFC stage and a dc/dc stage. They have been widely applied in adaptors for laptops and silver box [11]–[14]. Single-stage PFC converters integrate the PFC stage and the dc/dc stage, leading to simple topology and low cost. They are suitable for low-power applications [15]–[18]. Unfortunately, no effective method has been proposed to significantly reduce the storage capacitance in a PFC converter such that a long lifetime of a converter can be achieved.





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- 阐述所提出方法
- 相关理论分析
- 仿真和实验验证

#### II. Increase of the Voltage Ripple to Reduce the Storage Capacitor

- A. Relationship Between Voltage Ripple and Storage Capacitance of a Two-Stage PFC Converter
- B. Maximum, Minimum, and Average Values of the Storage Capacitor's Voltage
- C. Design Example

#### III. Injection of Harmonic Current to Reduce the Storage Capacitor

- A. Relationship Between Storage Capacitance and Input Power Factor
- B. Relationship Between Third Harmonic Injection and Storage Capacitance
- C. Control Method of Third Harmonic Injection
- IV. Experimental Verification



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作者简介

- 对本文工作进行小结,简要概括本文所提出的新方法、新理论和新技术,说明它们的优点。
- 可分1, 2, ..., n点, 也可分几段。
- 不要含有参考文献,公式和图表。
- 部分内容可与摘要重复。



作者

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作者简介

This paper proposes two methods that can be used to significantly reduce the storage capacitance, so that film capacitors, instead of electrolytic capacitors, can be adopted to achieve a long lifetime of the PFC converter. In a two-stage PFC converter, a storage capacitor is used to balance the pulsating input power and the dc constant output power. To significantly reduce the storage capacitor, the voltage ripple of the storage capacitor can be intentionally increased. However, a very large voltage ripple can result in the unstable operation of the boost PFC converter and a very high voltage stress on the power switches. This paper gives the derivation of the relationship between the voltage ripple and the value of the storage capacitor, and proposes a guideline for the selection of a suitable value for the storage capacitor. Moreover, a method of injecting the third harmonic current into the input current flow is proposed to reduce the pulsation of the input power. This can further reduce the storage capacitance. While keeping the input power factor at 0.9 to comply with the requirement of ENERGY STAR, the storage capacitor can be reduced to 65.6% of that with an input power factor of 1. A 60-W prototype was built and tested. Experimental results validated the feasibility of the proposed methods.



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- 本文工作的资助方。
- 参与讨论并提供有价值建议的人员。
- 提供功率器件及相关设备的公司和人员。
- 帮助样机制作的技术人员。
- 相关测试人员。
- .....

#### **ACKNOWLEDGMENT**

- The authors would like to thank **the National Natural Science Foundation of China (NSFC)** for the financial support under the award 50837003.
- The authors would like to thank **S. Y. Lam** for able assistance in developing the experimental prototypes.
- The authors would like to acknowledge the contribution of Golden Regent Electronics Industrial Limited for making the high-voltage transformer.
- The authors would like to thank **Dr. S.-C. Tan** from Hong Kong Polytechnical University, Kowloon, Hong Kong, for the revision of this paper.



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参考文献

作者简介

- Style for conference publications: Authors (first initials followed by last name), title of paper, in title of conference, date of conference, and page numbers (inclusive).
- [1] A. B. Researcher and I. N. Elper, "Loss-based analysis of switching converters under closed-loop controls," in *Proc. IEEE Applied Power Electronics Conf.*, 2010, pp. 3917-3926.
- **Style for periodical journal publications:** Authors (first initials followed by last name), title of paper, title of periodical, volume, page numbers (inclusive), month and year.
- [2] C. D. Aodiet, R. G. Gue, and P. R. Phakter, "The Ran-Duga method for ac-ac converter operation," *IEEE Trans. Power Electronics*, vol. 37, pp. 7721-7727, October 2014.
- **Style for books:** Authors (first initials followed by last name), book title. Publisher location: publisher name, year, chapter or page numbers.
- [3] B. B. Eriffel, *The Design of Microwatt Power Devices*. London: Energy Unit Publishers, 2012, Chap. 72.



作者

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参考文献

作者简介

Xinbo Ruan (M'97-SM'02) was born in Hubei Province. China, in 1970. He received the B.S. and Ph.D. degrees i 姓名,出生地点、日期,学历 ersity of Aeronautics and Astronautics (NUAA), Nanjing, China, in 1991 and 1996, respectively.

In 1996, he joined the Faculty of Electrical Engineering Teaching and Research Division, NUAA, where he became and has been engaged in Lacianing and Teaching and Electronics. From August to October 2007, he was a Research Fellow in the Department of Electronics and Information Engineering, Hong Kong Polytechnic University, Hunghom, Hong Kong. Since March 2008, he has been with the College of Electrical and Electronic Engineering, Huazhong University of Science and Technology, Wuhan, China. He has authored or coauthored more than 10 大夫论文和获得专利 ces and also published three books. His current research microsis metude sort-switching inverters, power electronics system integration, and renewable energy generation system.

Dr. Ruan was the recipient of Delta Scholars to Delta Frainment and Education the Chang Jiang Scholars Program, by the Ministry of Education, China, in 2007. He is also the Guest Professor of Beijing Jiaotong University, China, and Hefei University of Technology, China. Since 2005, he has been the member of the Technical Committee on Renewable Energy Systems within the IEEE Industri

Electronics Society and





**当**写科技论文的意义



**2** 科技论文的基本结构



**国际期刊评审论文的标准** 



立提高论文质量的方法



当结语



#### **Basic Qualification Questions**

• What is the **new contribution** of this paper? (Based on your assessment rather than on author statements.)

Does the contribution have good archival value, or is it only an incremental addition to existing knowledge?



## EE Transactions on Power Electronics 的评阅表

### **Paper Grading**

Original, elegant	5,, 0	Restatement of existing knowledge
Valuable for practicing engineers	5,, 0	Impractical or excessively commercial
Technically and mathematically accurate	5,, 0	Unsound; contains significant errors
Well supported with analysis/experiments	5,, 0	Unproven, unsupported
Rich in engineering judgement and insight	5,, 0	Uninformed, amateurish
Clear, concise, effective presentation	5,, 0	Obscure, disorganized, verbose
Interesting to readers, stimulates new ideas	5,, 0	Uninteresting; topic is nearly cliche
Effective illustrations and tables	5,, 0	Poor figures or figures without discussion
Correct English usage	5,, 0	Weak grammar; difficult to follow
Useful references to past work	5,, 0	No context is provided beyond the authors' work



## EE Transactions on Power Electronics 的评阅表

#### Recommendation

	Accept without change - The paper can be published in its present form.
	Accept with suggested change - The paper can be published in its current form but could be made stronger by incorporating changes suggested by the reviewers.
	Accept with mandatory changes - The paper cannot be published in its current form but can be provisionally accepted if the authors incorporate mandatory changes suggested by the reviewers. It is the opinion of the reviewers that the changes are relatively minor and can be incorporated in ten weeks or less.
	<b>Do not accept</b> - The paper cannot be accepted in its current form. The reviewers should indicate why the paper cannot be accepted and suggest the paper could be accepted after a major revision.
0	Accept
0	Minor Revision
0	Major Revision
0	Reject & Resubmit
0	Reject



## E Transactions on Power Electronics 的评阅表

#### **Comments**

Confidential comments to the Editor.

Comments to the Author

















第一步: 撰写论文主体部分(不包括引言)

第二步: 撰写结论

第三步: 撰写引言

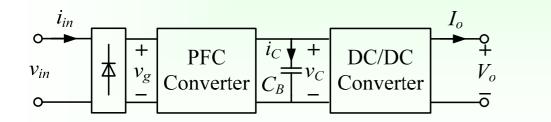
第四步: 撰写摘要

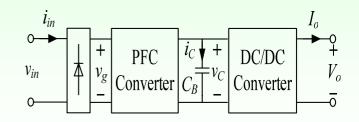
第五步:确定论文题目





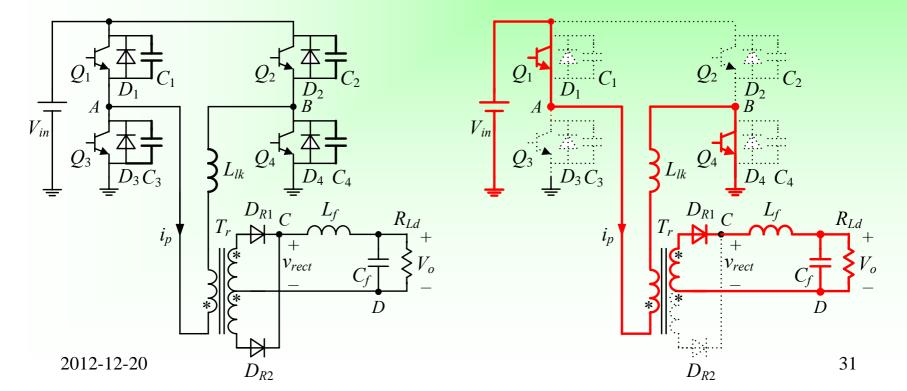
- 画图工具: Microsoft Office Visio 2003
- 图形尽量紧凑,以节约版面,同时保证缩小时字体不会太小。
- 图形应美观大方,给人的第一感觉是看到图,然后才是字符和标注, 否则有喧宾夺主之虞。
- 图形放到正文中时,其**字符原则上不能大于正文文字大小**。画图时,建议字符和公式的字体用**Times New Roman**,字号用**10pts**,这样贴到论文中时,可以不缩小或缩小到90%。(论文正文字号一般为10pts)
- 图形缩放时,应成比例缩放,以免变形。







- Visio软件自带的元器件库有的不太美观,使用时也不便于自己编辑。 建议自己设计元器件库。
- 一般的线条粗细选择3pts,强调时选择为5pts。
- 工作的元器件用实线,不工作的用虚线(10#线型较为美观)。



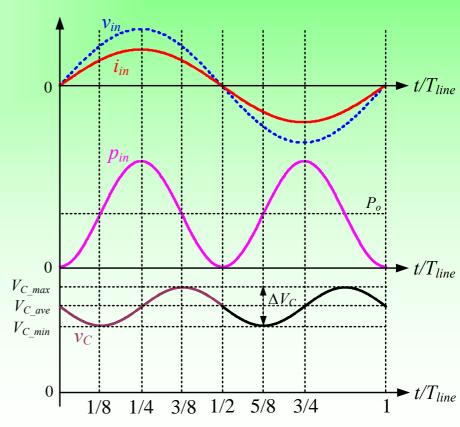




• 直线一定要直,不能斜。

 有规律的线型尽量准确,比如正弦波形,可以利用Mathcad软件绘出, 再copy到Visio里编辑。

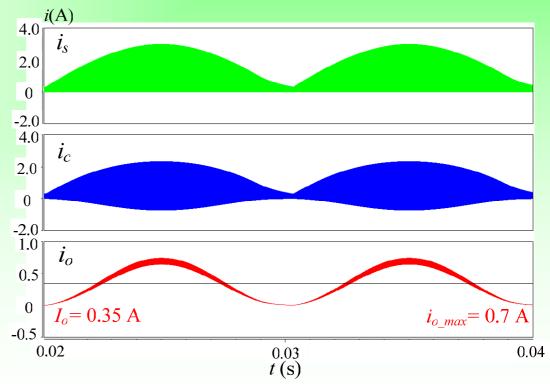
- 工作波形用实线,5pts;用来 标注的线型用10#虚线,3pts。
- 工作波形的零位一定要标出。以便于看懂。
- 每个波形的符号必须标出,标 注位置视具体情况而定,不一 定标在纵座标处。
- 横坐标,如时间轴,必须标出。







- 仿真或计算得到的波形或曲线,所采用的软件自身会给出相应的符号和标注的字体和大小并不一定适合直接贴在正文中,为此可以将其copy到 Visio中进行处理。
- 可将波形或曲线中不必要的部分裁减掉,补充适当的横坐标和纵座标刻度。
- 波形符号可在图形的相应 位置标出,原则上应在垂 直方向对齐。



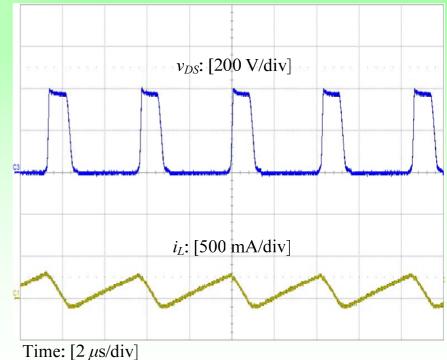




示波器测得的波形由于探头衰减等原因,其每个波形的标尺不太直接。
 因此可将该图copy到Visio中进行处理。

可将波形中不必要的部分裁减掉,但是每个波形的零位必须保留,以 便于阅读波形的幅值大小。

- 在Visio中给出每个波形的纵座 标标尺,同时给出时间标尺。
- 如果波形很多,如果每个波形中都给出纵座标标尺,可能会使得字符比较拥挤,甚至会压到波形。此时波形中可不给出标尺,标尺可在图说中给出。





- 写作引言之前,应收集、整理自己已阅读过的相关文献。
- 阅读**拟投稿的期刊**近**2~3年**发表的论文的目录,从中找出与本文相关的 文献,并仔细阅读,尽量加以引用。
- 尽量引用与本文内容相关的国际上知名学者的论文。
- 同一篇论文如果既发表在会议论文集中,又发表在期刊中,原则上只列 出发表在期刊上的论文。
- 引用的参考文献尽量给予适当的评价和说明,并在相关处标注其编号。
- 除非是综述性的论文,参考文献一般列写15~20篇。综述性的论文参考 文献数量可适当多一些。



- 在写作之前,最好阅读几篇拟投稿期刊的论文,以了解期刊论文的大致写作方法。
- 阅读拟投期刊的投稿须知(Guidelines for Authors/Information for Authors),了解论文投稿的格式和要求。
- 论文的题目、作者、摘要、关键词、小节题目、参考文献等的字体、大小、大小写、是否斜体等要严格按照要求。
- 有的期刊(如IEEE Transactions on Power Electronics)要求投稿的论文采用单栏、2倍行间距排版,而有的期刊(如IEEE Transactions on Industrial Electronics)要求双栏、单倍行间距排版,此时图表应放在左上角、右上角、左下角或右下角,即"顶天立地",原则上多个图集中放在一起。



- 论文投稿后,经相关审稿专家审稿后,会回馈审稿意见。对于审稿意见 要认真阅读,并严格按照审稿人的意见和建议进行修改,切忌与审稿人 辩论!
- 修改论文时,应在相应出用粗体或颜色标出。
- 撰写修改说明。针对审稿人的每条意见说明修改情况。语气要客气、谦虚,切不可流出不满的情绪。对于无法补充或修改的地方要尽量说明原因。修改说明要尽量简洁!
- 请记住,国际期刊的审稿人都是Volunteer,没有任何报酬,全是义务劳动。他们花费大量时间阅读我们的论文,并给出意见和建议,我们要心存感激。



#### Notes to the Reviewers/Associate Editor/Editor

The authors would like to thank the reviewers, the Associate Editor, and the Editor-in-Chief for your constructive remarks and suggestions. A revision has been carefully performed on this paper according to the suggestions provided. The following are the changes made in the revised paper.

#### **Reviewer: 1**

There are two points mentioned in the reviewer's comments, and our responses are:

1. But, a very long part of the paper is reserved for description of well known power converters. Presentation of power converters and source association rules is too basic.

Answer: We have tried to condense Section II, and have deleted Fig. 2 of the original version, so that this part becomes more concise.

2. Power rate and constraints as cost, efficiency and integration are not considered in the synthesis, while they are essential.

Answer: We have added a paragraph at the end of Section VIII (page. 18) to address the power rating and constraints as cost, efficiency and integration. However, we should stress that the aim of this paper is a circuit theoretic approach for synthesizing circuits.

#### **Reviewer 2**

• • • • •





- 应强调质量,而不是数量。不要为了论文而写论文!
- 绝对不可以**剽窃**他人的研究成果,不可以**伪造数据**。
- 署名应按贡献大小与所有合作作者协商排序,**杜绝挂名**。
- 论文在投稿之前、审稿意见返回后、 提交最终稿件和排版清样审校时,应 送所有合作作者审读和修改,这是对 合作作者的尊重,而对论文的修改也 是合作作者的权利和义务。







· 科技论文的基本结构

**四**国际期刊评审论文的标准

立提高论文质量的方法





- 论文写作是对自己研究工作的一个总结,也是研究过程的 一个重要组成部分。
- 发表论文是对本领域学术界和工业界的贡献,有利于建立 自己的学术地位,提升自己的自信。
- 学术论文贵在创新性,重在准确性、系统性、完整性,要 清晰而简洁,尤其要注意写作规范。



## 谢谢各位!

## 祝各位写出高质量的学术论文!

